



2186

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kleint, et al. Docket No.: 2002 P 14454 US  
Serial No.: 10/735,411 Art Unit: 2186  
Filed: December 12, 2003 Examiner: TBD  
For: Method for Fabricating Semiconductor Memories with Charge Trapping  
Memory Cells

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Respectfully submitted,

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